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Application No. 10/735,904
Amendment due November 1, 2006
Reply to Office Action of August 1, 2006

Docket No.: 2019-0232P

REMARKS

Claims 1-3, 5-8 and 11-13, 16 are now present in this application.

The specification and claims 1, 8, 12 and 13 have been amended, claims 4, 9, 10, 14 and 15 have been cancelled without prejudice or disclaimer, and claim 16 has been added. Reconsideration of the application, as amended, is respectfully requested.

Rejection under 35 USC 112

Claims 1-7 stand rejected under 35 USC 112, second paragraph. This rejection is respectfully traversed.

In view of the foregoing amendments, it is respectfully submitted that all claims particularly point out and distinctly claim the subject matter of the instant invention. Reconsideration and withdrawal of the 35 USC 112, second paragraph rejection are respectfully requested.

Rejection under 35 USC 102

Claims 1-4, 6, 7 and 12-15 stand rejected under 35 USC 102(b) as being anticipated by Someya et al., U.S. Patent 5,396,257. This rejection is respectfully traversed.

Someya discloses a multiscreen display apparatus in which a single video image is split into a plurality of separate signals, each signal corresponding to a core such as a CRT (cathode ray tube) in a multiscreen display apparatus. The signals are converted so as to electrically correct the luminescence thereof, so as to reduce the luminance shading and color shading

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between the plurality of display units. In other words, a single video signal is split into separate portions, each portion being displayed on a different monitor (see column 2, lines 39-62).

Independent claim 1 of the present application, however, recites a CRT controller for receiving a first video signal having a first color data and converting a plurality of second video signals having second color data from the first video signal. In particular, the first video signal has first color data and the second video signal has second color data. The size of the second color data is less than a size of the first color data, and the second video signals has the same second color data. This is unlike Someya et al., in which each of the video signals is different.

Independent claim 12 of the present application, recites a CRT controller for converting a plurality of digital video signals and outputting a vertical/horizontal synchronization signal to the monitors, wherein the each of the digital video signals has the same digital video. This is also unlike Someya et al., in which each of the video signals is different.

Accordingly, it is respectfully submitted that the prior art utilized by the Examiner fails to teach or suggest the method of independent claims 1 or 12, as well as their dependent claims. Reconsideration and withdrawal of the 35 USC 102 rejection are respectfully requested.

Rejection under 35 USC 103

Claims 5 and 8-11 stand rejected under 35 USC 103 as being unpatentable over Someya in view of the Applicant's disclosed description of the prior art (ADDDPA). This rejection is respectfully traversed.

The Examiner has acknowledged that Someya fails to teach a CRT controller with a graphics engine, or a motherboard for multiple monitors comprising a chipset for outputting a

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plurality of image signals, but that the ADDPA teaches these features. It is respectfully submitted, however, that the ADDPA fails to overcome the above-noted deficiencies of the primary reference to Someya et al. Accordingly, reconsideration and withdrawal of the 35 USC 103 rejection are respectfully requested.

Conclusion

Favorable reconsideration and an early Notice of Allowance are earnestly solicited.

Because the additional prior art cited by the Examiner has been included merely to show the state of the prior art and has not been utilized to reject the claims, no further comments concerning these documents are considered necessary at this time.

In the event that any outstanding matters remain in this application, the Examiner is invited to contact the undersigned at (703) 205-8000 in the Washington, D.C. area.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension of time fees.

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Respectfully submitted,

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